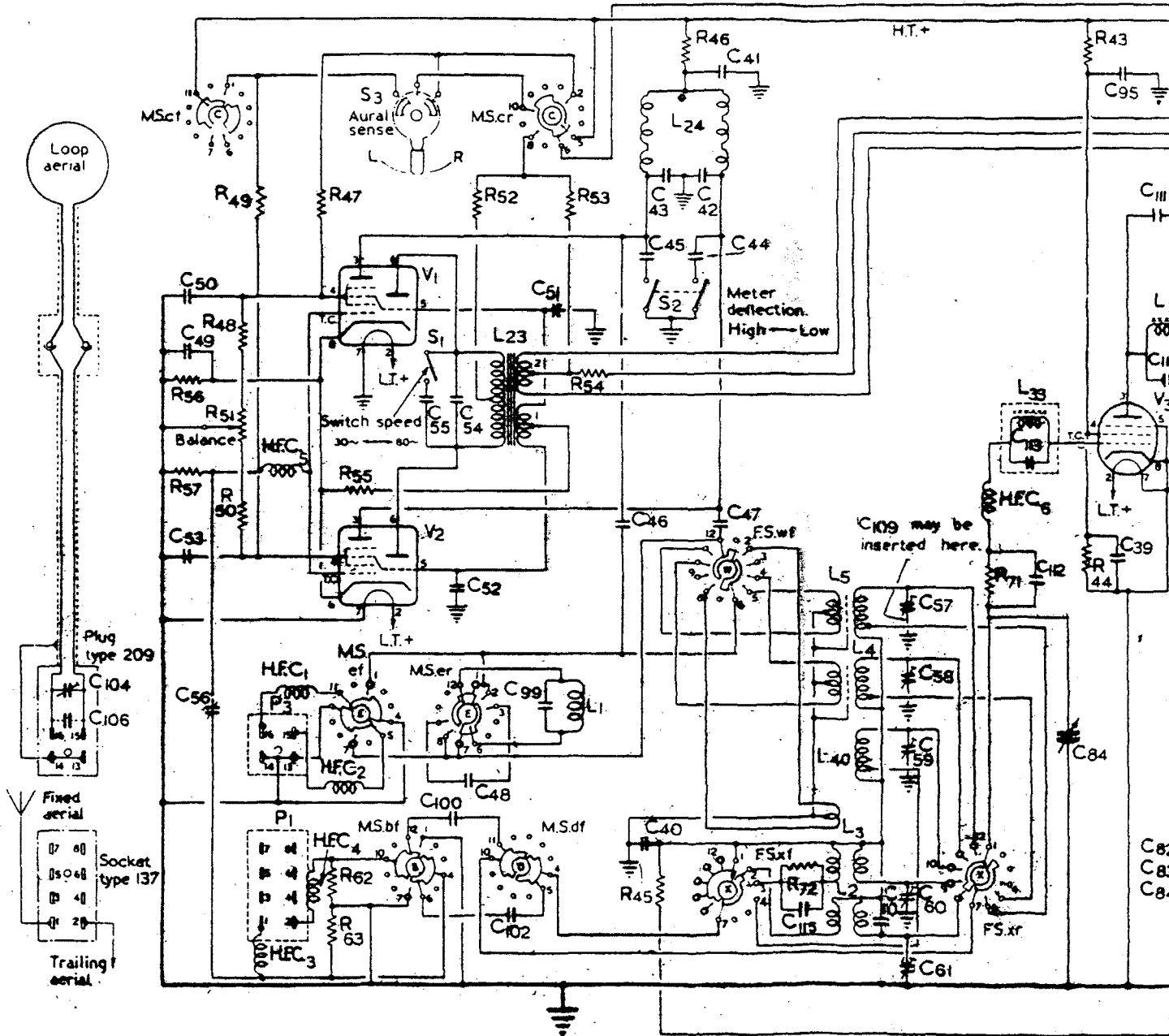


C104 C106	C50 C49 C53	C56 C53	C55 C54 C102 C99 C51 C40—C47 C100 C48 C52	C57—C61 C115 C110 C112 C113 C39 C	C84 C95 C
R56 R48—R51 R57	R47 R55 R52 R63	R52 R53 R54 R46 R45	R72	R71	R43 R44
MScl	P ₃ HFC ₁ —HFC ₅ V ₁ S ₃ S ₁ MSer L ₂₃ MScr P ₁ MSel V ₂ MSbf MSdf L ₁	L ₂₄ FS.wf S ₂ FS.xf	L ₄ —L ₅ L ₄₀	HFC ₆ FS.xr L ₃₃	V ₃ L

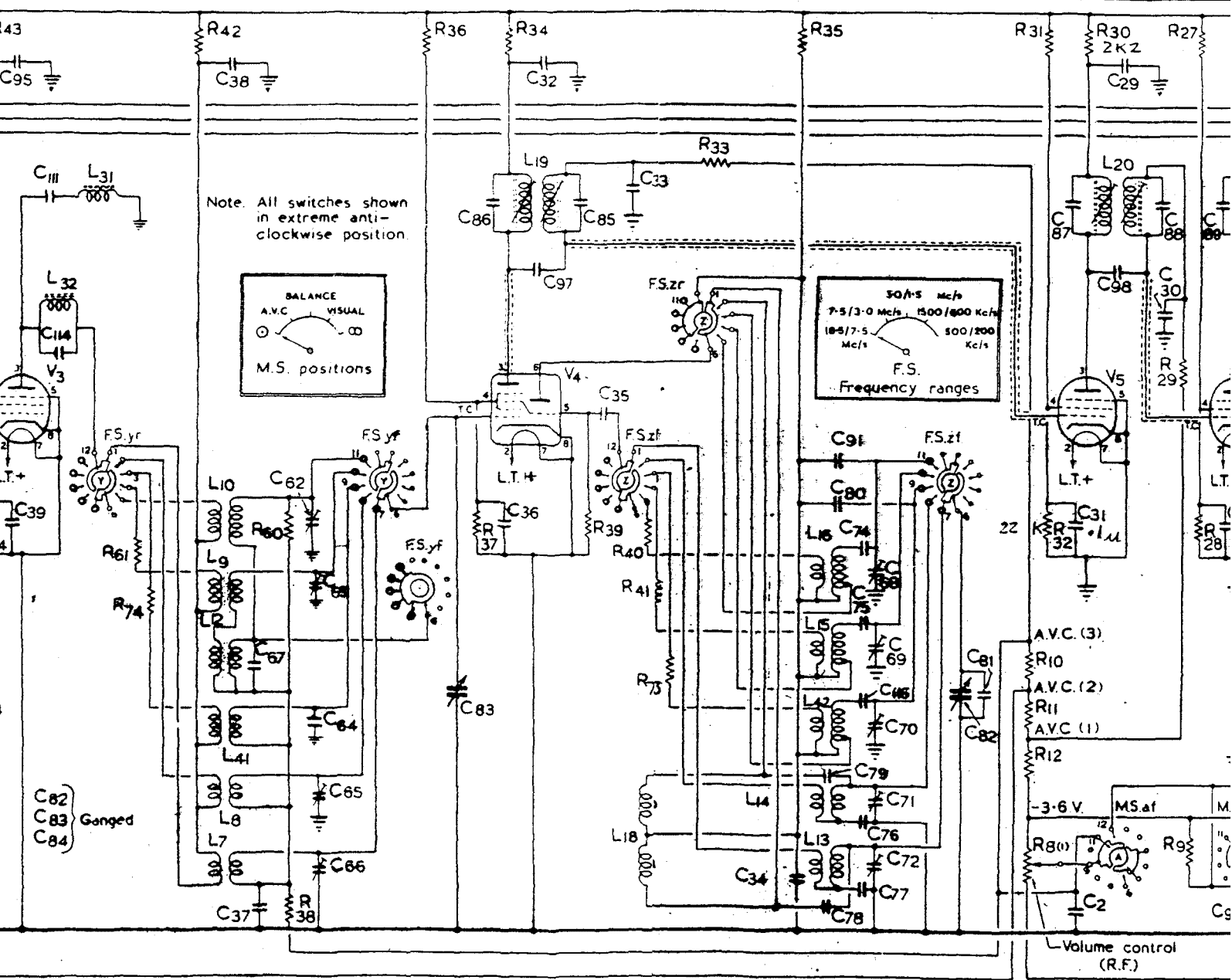


Note. Switch contacts shown thus a, denote front (f) & rear (r) contacts connected. Important. In order to avoid excessive crossing of connecting wires certain switch wafers have been duplicated.

FIG. 4

A.P. 2548 A VOL I CHAP. 2

C95 C39	C111 C114	C38 C37	C67 C62-C86	C83 C36	C86 C97	C32 C85	C85 C35	C33 C35	C34 C116	C68-C72 C74-C80	C91 C82	C81 C81	C87 C31	C29 C2	C88 C98	C89 C3C	C1 C28							
R61 R74	R42	R60 R38	R36	R34 R37	R39	R40 R41	R33 R73	R35	R10-R12 R80	R31 R32	R30 R30	R27-R29 R9	V3	L32 L31 FSyr	L7-L10 L41	L12	FSyr FSyf	L19 V4	FSz1 L18	FSzr	L13-L16 L42	FSz1	V5	L20 MSaf



R 1155 L AND R 1155 N CIRCUIT DIAGRAM

