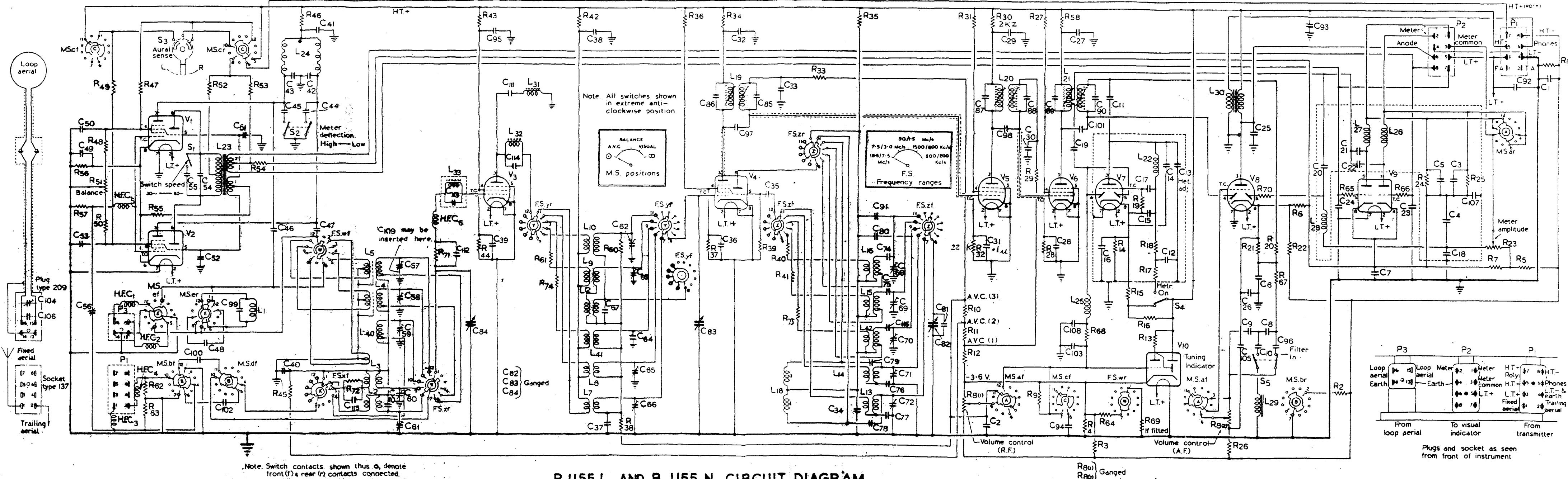


C104 C106	C50 C49	C56 C53	C55 C100	C54 C48	C102 C48	C99 C52	C51 C52	C40 C47	C47	C57-C61 C115	C110	C112 C113	C84 C39	C95 C114	C111	C38 C37	C67 C62-C66	C67	C83 C36	C86 C97	C32 C85	C33 C35	C34 C116	C68-C72 C74-C80	C91	C82 C81	C87 C31	C29 C2	C88 C98	C89 C30	C9 C28	C101 C94	C27 C103	C90 C16	C17 C15	C14 C12	C13	C8-C10 C105	C96 C26	C93 C25	C93 C20	C24 C22	C21 C7	C7	C23 C3-C5	C18	C107	C92	C1
R56 R57	R48-R51 R62	R47 R63	R52	R53 R54	R46 R45	R72	R71	R43 R44	R61 R74	R42 R60	R36 R37	R34 R39	R40 R41	R33 R73	R35	R10-R12 R80	R31 R30	R27-R29 R58	R68 R4	R13 R3	R19 R69	R8(2) R26	R20 R70	R22 R67	R6	R65	R66 R24	R25	R23 R7	R5	R1																		
MSct P1	P3 HFC1 MSel	V1 V2	S3 S1	MSer MSdf	L23 L1	L24 FSwf FSxf	L4-L5 L40	HFC6 FSxr	V3 L32	L31 FSyr	L7-L10 L41	FSyr FSyf	L19 V4	FSzr L18	L13-L16 L42	FSzr	V5 L20	MSaf	V6 L21	L25 V7	S4 L22	MSaf L22	V10	MSaf L30	S5 L29	MSbr L28	L27 L26	V9	P2	P1 MSar																			



R 1155 L AND R 1155 N CIRCUIT DIAGRAM

FIG. 4

FIG. 4